

# PRIME

Power-efficient, Reliable, Many-core Embedded systems



## PRIME Showcase Demonstrator 2018 - Theme 4

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## INTRODUCTION

PRIME partners are working on diverse and interlinked topics that often need different algorithms and applications.

Goal: The PRIME showcase demonstrator will bring together contributions from all themes into a single demonstrator:

- >Showing how they work together;
- ➤ Demonstrating the vision of PRiME.

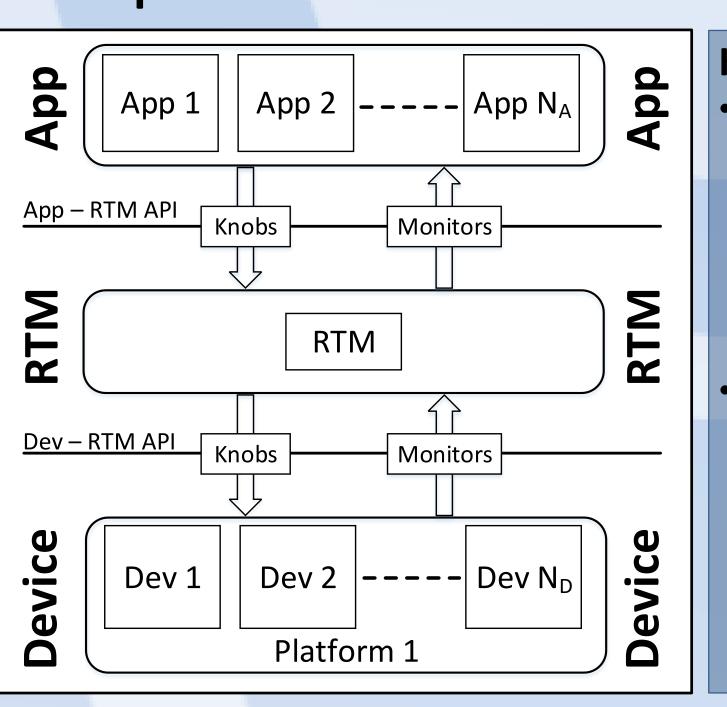
Not every PRiME output/contribution is (necessarily) incorporated in the PRiME showcase demonstrator.

illustrates PRIME demonstrator the Framework is a fundamental instrument in linking the different PRIME contributions by:

- Using different Run-Time Management Algorithms,
- To run different Applications,
- On different Platforms,
- Incorporating aspects of Modelling and Verification

## **The PRiME Framework**

The PRiME Framework considers a system as three separate layers (Application, Runtime Management and Device) that are linked with a defined API that abstracts communication through the exposure of knobs and monitors.



### **Knobs & Monitors:**

- runtime-tunable parameters. A knob is composed of a initial, minimum & maximum values.
- e.g. voltage, frequency & iterations.
- expose observable metrics about the behavior of the device and application. Monitors are either discrete or continuous and have fields for value, target range (minimum and maximum) and a weighting.
- e.g. temperature, power & performance.

This allows Runtime Management Algorithms to be platform and application agnostic.

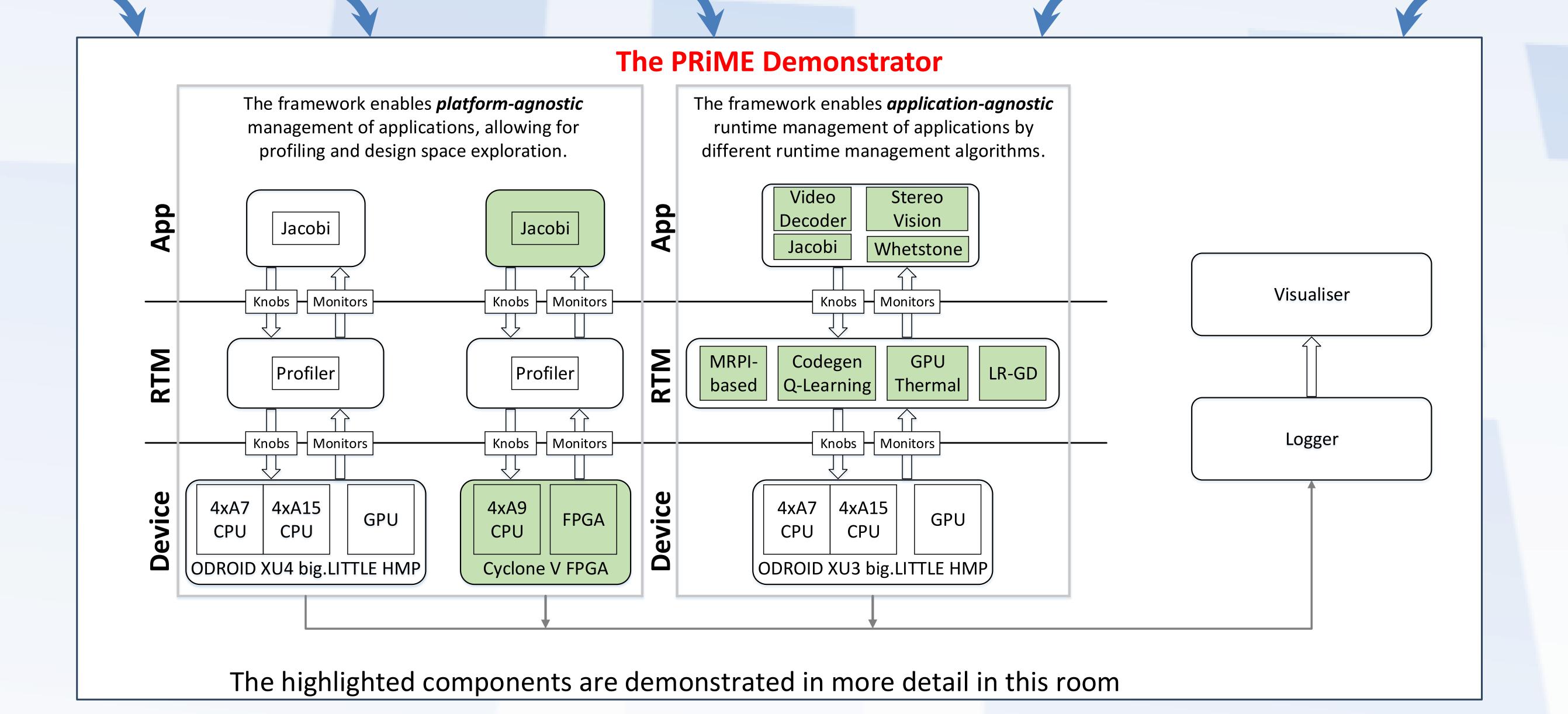
Each app/platform has different knobs and monitors exposed through the PRiME Framework:

		Module	Knobs	Monitors
	Application	Jacobi	Iterations, Precision, Device	Performance (throughput), Accuracy
		Video Decoder	_	Performance (FPS)
		Whetstone	Threads	Performance (KIPS)
		Stereo Vision	_	Performance (FPS)
	Device	Odroid XU3	CPU Frequency, GPU Frequency, Governor selection	CPU & GPU Power, CPU & GPU Temperature, ARM PMCs
		Odroid XU4	CPU Frequency, GPU Frequency, Governor selection	CPU & GPU Temperature, ARM PMCs,
		Cyclone V	CPU Voltage, FPGA Voltage	SoC Power, CPU Power, FPGA Power

## Profiling Platforms

Runtime Algorithms

Formal Verification Applications



Imperial College London













